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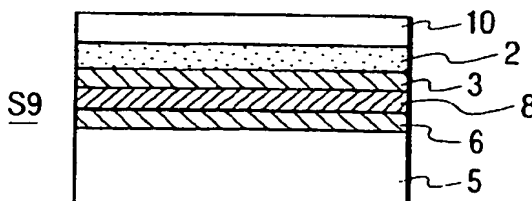
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(54) **Method of preparing a SOI substrate by using a bonding process, and SOI substrate produced by the same**

(57) A semiconductor substrate adapted to giga-scale integration (GSI) comprises a support, at least the surface of which is made of semiconductor, an electroconductive material layer, an insulating layer and a semiconductor layer arranged sequentially in the above order. The electroconductive material layer has at least in part thereof an electroconductive reacted layer obtained

by causing two metals, a metal and a semiconductor, a metal and a metal-semiconductor compound, a semiconductor and a metal-semiconductor compound, or two metal-semiconductor compounds to react each other. An electroconductive reaction terminating layer that is made of a material that does not react with the reacted layer is arranged between the reacted layer and the insulating layer or the support.

FIG. 11



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EUROPEAN SEARCH REPORT

Application Number
EP 98 30 5994

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A	US 5 102 821 A (MOSLEHI MEHRDAD M) 7 April 1992 (1992-04-07) * abstract; claims 1,3,4 * * column 1, line 27 - line 31 * * column 2, line 17 - line 45 * * column 4, line 10 - line 15 * ---	1-44	
A	EP 0 256 397 A (HITACHI LTD) 24 February 1988 (1988-02-24) * figures 3A1,3A2 * * column 4, line 51 - column 5, line 15 * * claim 4 * ---	1-44	
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The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 26 November 1999	Examiner Le Meur, M-A
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document but published on, or after the filing date D : document cited in the application L : document cited for other reasons S : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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